LAN89218 Mini Development Card

Assy 6519

PCB Revision A

Schematic Revision 1.1

Design Details

Board:
Assy 6519

Chip:
SMSC LAN89218

Board Form Factor:
Larger than 2.346" x 2.300"

Assembly:
100-Pin TQFP

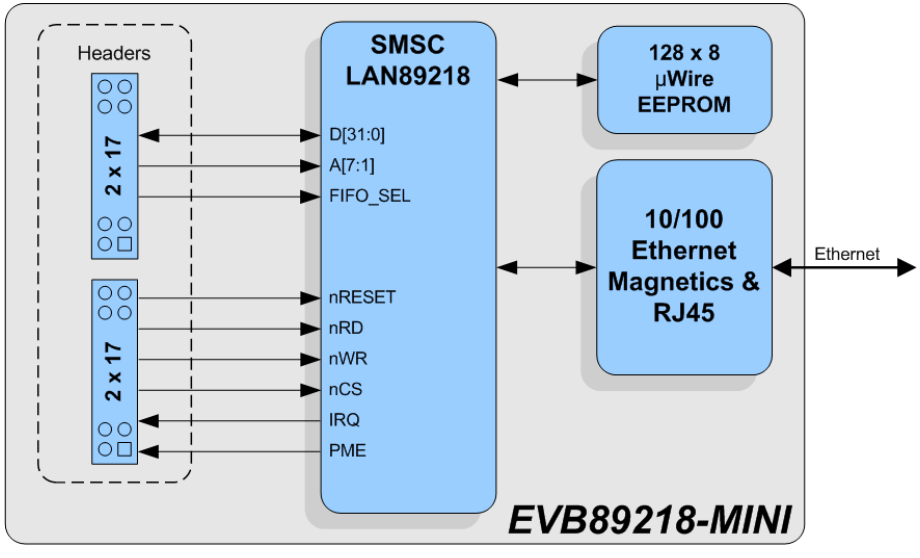
Important Note: Some support components shown in this design example may not be Automotive-Grade. Please be sure to check the manufacturer's datasheet(s) for detailed specifications and corresponding part numbers.

Revision History

Rev 1.0 to 1.1:

All Pages: Added note for non-Automotive-Grade support components.
Page 2: Added note for default ADDR1 configuration.
Page 2: Added note for EEPROM CS pull-down resistor.

BLOCK DIAGRAM



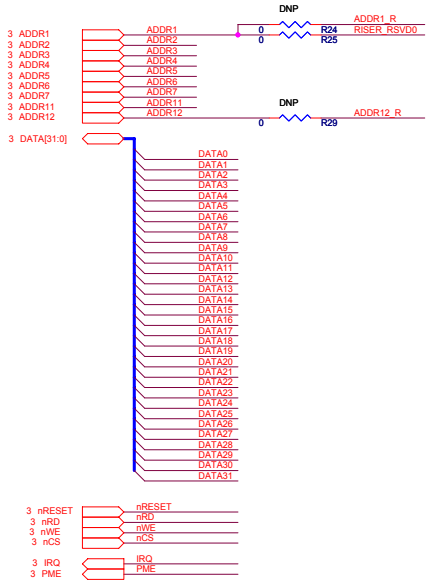
SMSC SUCCESS BY DESIGN

Title				
LAN89218 Customer Evaluation Board				
Size: C	Engineer: R. W.	Assembly No.: 6519	PCB Rev: A	Schematic Rev: 1.1
Date: Friday, December 17, 2010			Sheet 1 of 3	

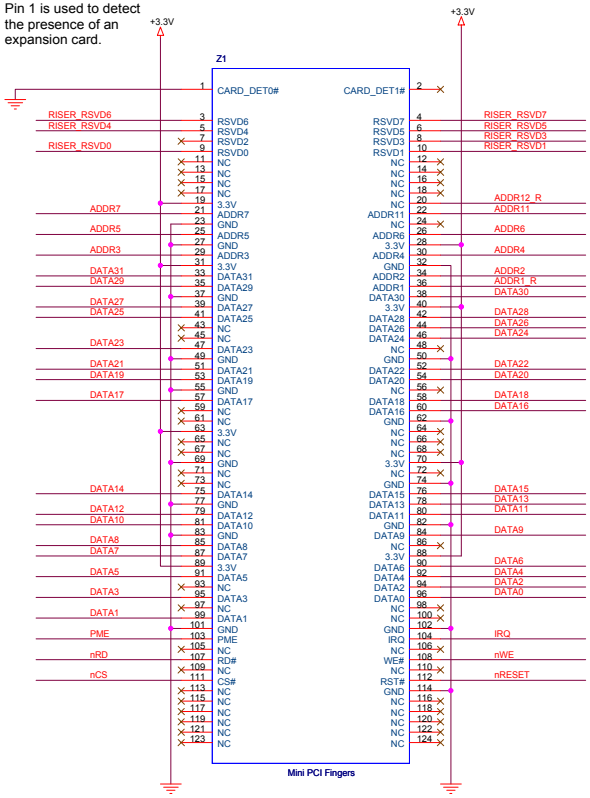
Important Note: Some support components shown in this design example may not be Automotive-Grade. Please be sure to check the manufacturer's datasheet(s) for detailed specifications and corresponding part numbers.

Local Bus Expansion Slot

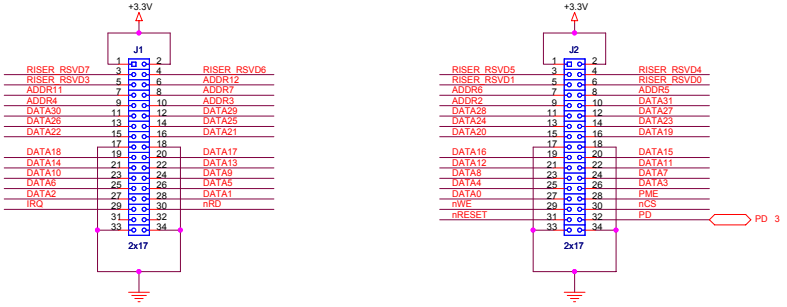
NOTE:
ADDR1 is connected to RISER_RSVD0 (Pin 9 on Z1) by default.
Remove R25 and populate R24 to connect ADDR1 to ADDR1_R (Pin 36 on Z1)

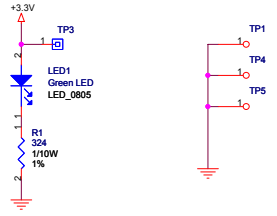


NOTE:
Pin 1 is used to detect the presence of an expansion card.



Local Bus Test Points

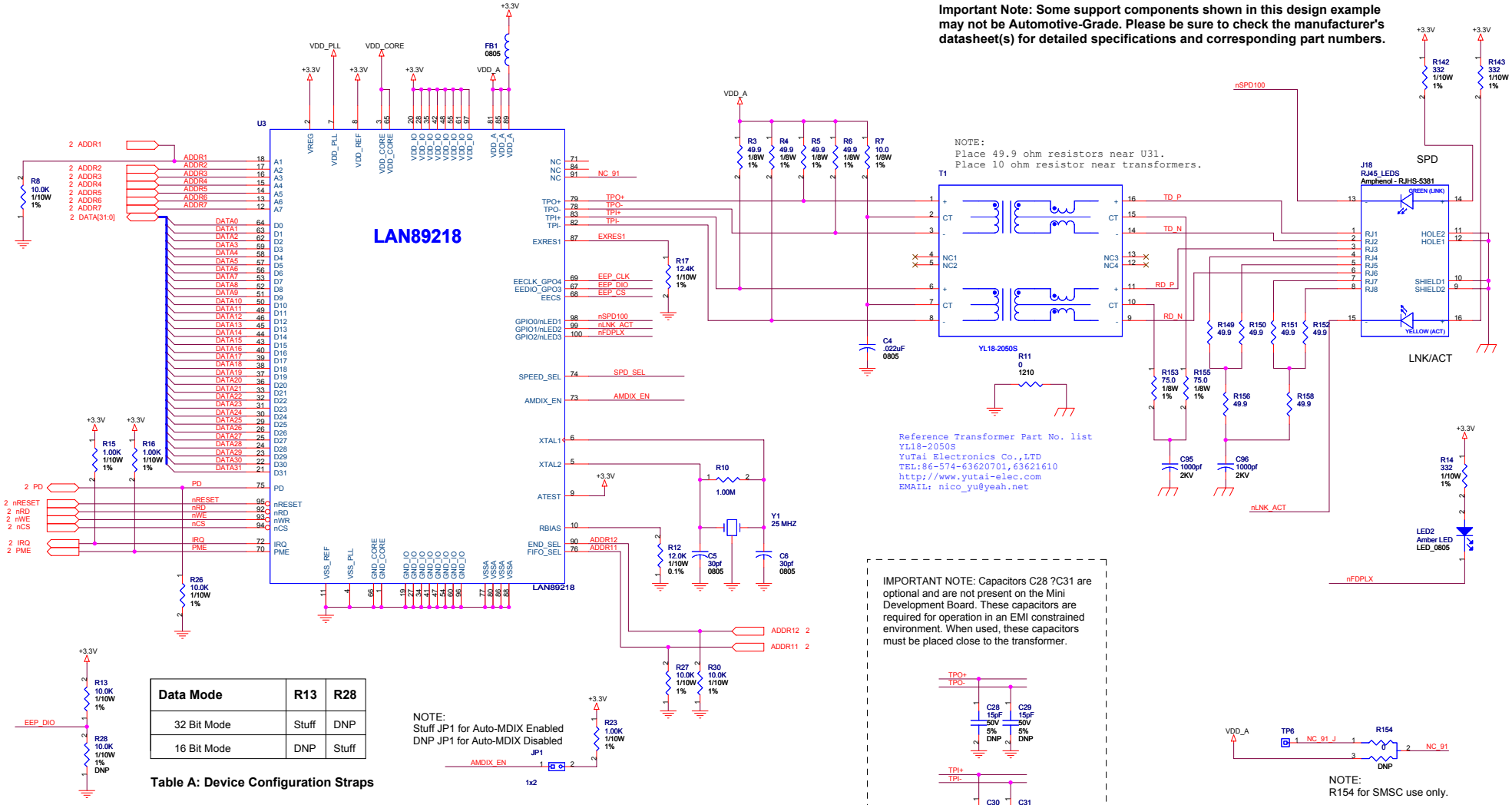




SMSC SUCCESS BY DESIGN

Title				
Expansion Slot Edge Fingers				
Size	Engineer	Assembly No.	PCB Rev	Schematic Rev
C	R. W.	6519	A	1.1
Date:	Friday, December 17, 2010		Sheet	2 of 3

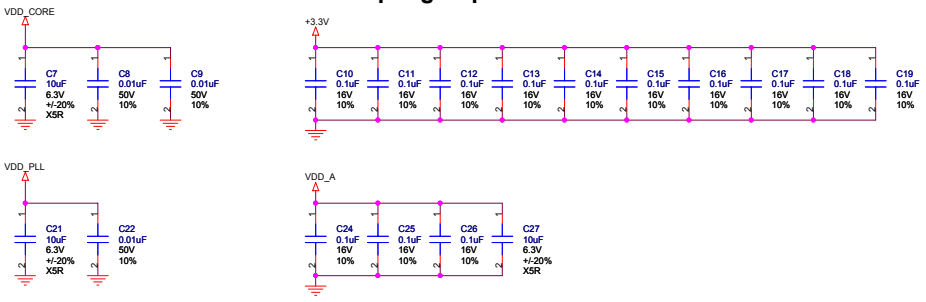
Important Note: Some support components shown in this design example may not be Automotive-Grade. Please be sure to check the manufacturer's datasheet(s) for detailed specifications and corresponding part numbers.



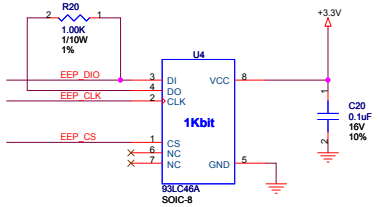
Data Mode	R13	R28
32 Bit Mode	Stuff	DNP
16 Bit Mode	DNP	Stuff

Table A: Device Configuration Straps

Decoupling Capacitors



MAC EEPROM (Optional)



NOTE:
A pull-down resistor on the EEPROM's chip select (CS) signal is recommended by many EEPROM manufacturers. Please refer to the EEPROM manufacturer's data sheet for further information.

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Size: C	Engineer: R. W.	Assembly No.: 6519	PCB Rev: A
Date: Friday, December 17, 2010		Sheet 3 of 3	Schematic Rev: 1.1